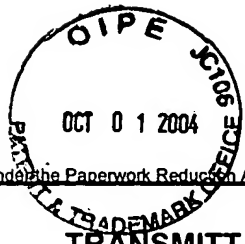


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	Filing Date	March 31, 2004	
	First Named Inventor	Chung J. Lee	
	Art Unit	2811	
	Examiner Name		
Total Number of Pages in This Submission	4+	Attorney Docket Number	DSI 301

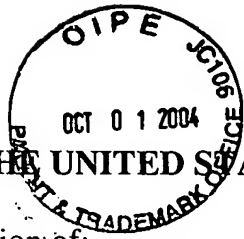
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	M. Matthews Hall, Esq. Kolisch Hartwell, P.C.
Signature	<i>M. Matthews Hall</i>
Date	September 28, 2004

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In re Application of:

Date: September 28, 2004

CHUNG J. LEE and ATUL KUMAR

Serial No. : 10/816,205

Group Art Unit 2811

Filed : March 31, 2004

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INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.56, 1.97, and 1.98

Applicants are submitting this Information Disclosure Statement pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98 to disclose to the U.S. Patent and Trademark Office the patents, publications, applications, and/or other references listed on the enclosed, completed PTO-1449 form(s). The filing of this Information Disclosure Statement should not be construed as a representation that a search has been made or as an admission that the listed references are prior art for this application. Applicants respectfully request that the listed references be expressly considered during prosecution of the application, and that the references be made of record therein and appear among the "references cited" on any patents issuing therefrom.

CONTENT OF DISCLOSURE

This Information Disclosure Statement includes (1) one page of PTO-1449 forms, and (2) a legible copy of each foreign and non-patent reference, if any, listed on the form(s). However, because this application was filed on or after July 1, 2003, no copies of U.S. patents or published U.S. patent applications are included.

TIMING OF DISCLOSURE / FEE INFORMATION

This Information Disclosure Statement is being filed, to the best of the undersigned's knowledge, either (1) before the mailing of a first Office action on the merits, or (2) before the mailing of a first Office action after the filing of a request for continued examination under 37 C.F.R. § 1.114. Therefore, in accordance with 37 C.F.R. § 1.97(b), no fee or statement under 37 C.F.R. § 1.97(e) is required.

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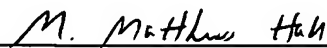
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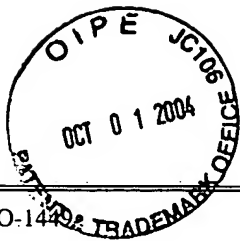
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Respectfully submitted,

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SHEET 1 OF 1

FORM PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION		DOCKET NUMBER DSI 301		APPLICATION NUMBER 10/816,205		
		APPLICANTS Chung J. Lee and Atul Kumar				
		FILING DATE March 31, 2004		GROUP ART UNIT 2811		
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FIL. DATE IF APPROP.
	3,503,903	3/31/70	Shaw et al.			
	6,086,952	7/11/00	Lang et al.			
	6,130,171	10/10/00	Gomi			
	6,265,320	7/24/01	Shi et al.			
	6,455,443	9/24/02	Eckert et al.			
	6,495,208	12/17/02	Desu et al.			
FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES NO
OTHER DOCUMENTS						
	<i>Parylene Copolymers</i> , Taylor et al., <u>Low Dielectric Constant Materials III</u> , pp. 197-205, 1997.					
	<i>Finer Copper Wires Make for Faster Integrated Circuits</i> , Preuss, <u>Research News</u> , pp. 1-3, April 5, 1999.					
	<i>Study of Hydrogen Annealing of Ultrahigh Molecular Weight Polyethylene Irradiated with High-Energy Protons</i> , Wilson et al., <u>Journal of Materials Research</u> , Vol. 14, No. 11, November 1999.					
	<i>A Novel Oxazole Based Low k Dielectric Addresses Copper Damascene Needs</i> , Schmid et al., <u>Semiconductor Fabtech</u> , 12 th Edition, pp. 231-235, July 2000.					
	<i>The Effect of Water Desorption and Organosilane Coupling Agents on the Adhesion of Poly(p-xylylene) Films to a Silicon Wafer Surface</i> , Lightfoot et al., <u>Journal of Materials Science: Materials in Electronics</u> , Vol. 12, pp. 581-586, 2001.					
	<i>Current Technical Trends: Dual Damascene & Low-k Dielectrics</i> , Healey on behalf of Threshold Systems, pp. 1-6, © 2002.					
EXAMINER			DATE CONSIDERED			